FIG. 1
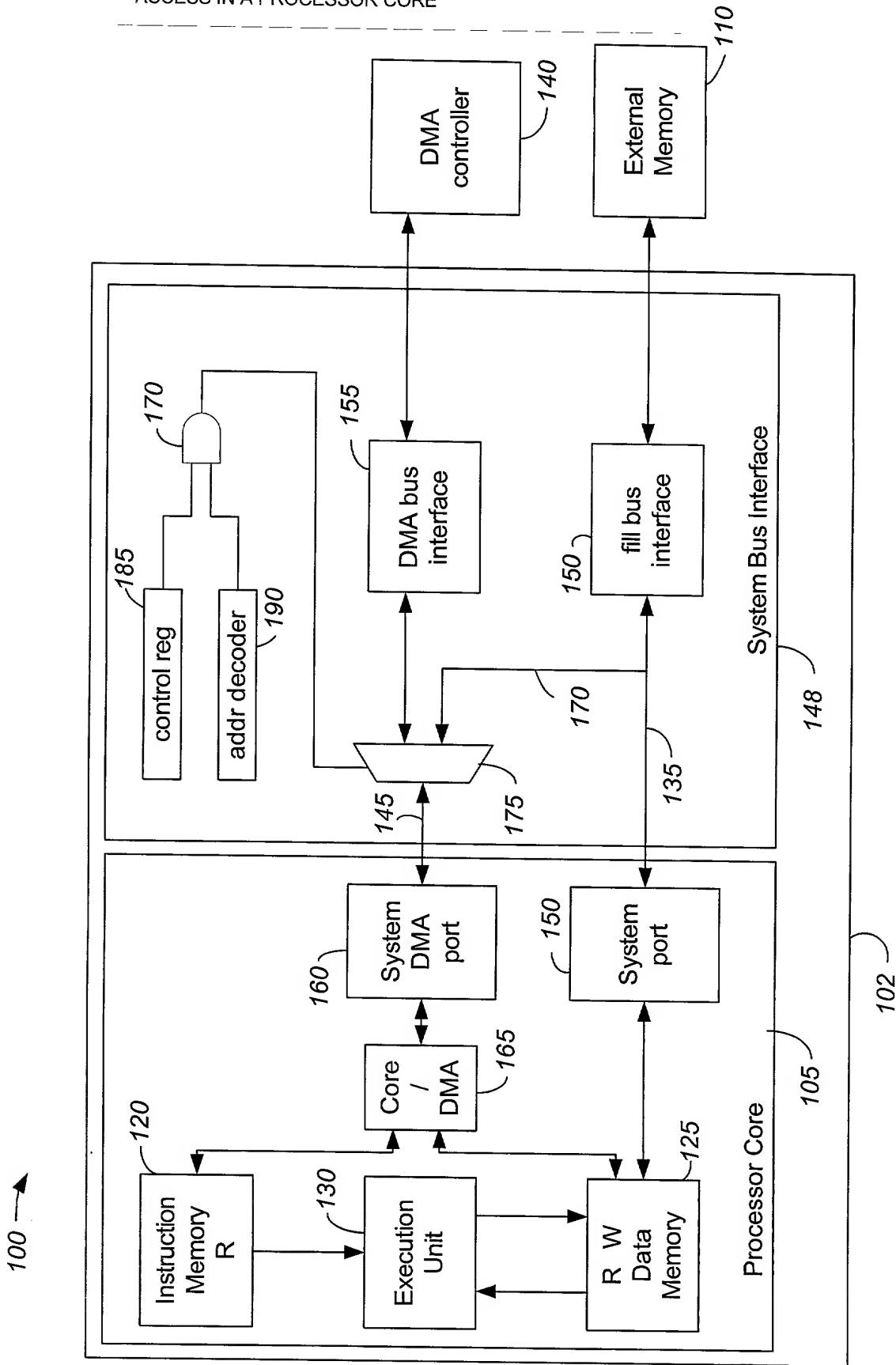
Applicant(s): Juan G. Revilla et al.
METHOD AND APPARATUS FOR CONVERTING AN
EXTERNAL MEMORY ACCESS INTO A LOCAL MEMORY
ACCESS IN A PROCESSOR CORE
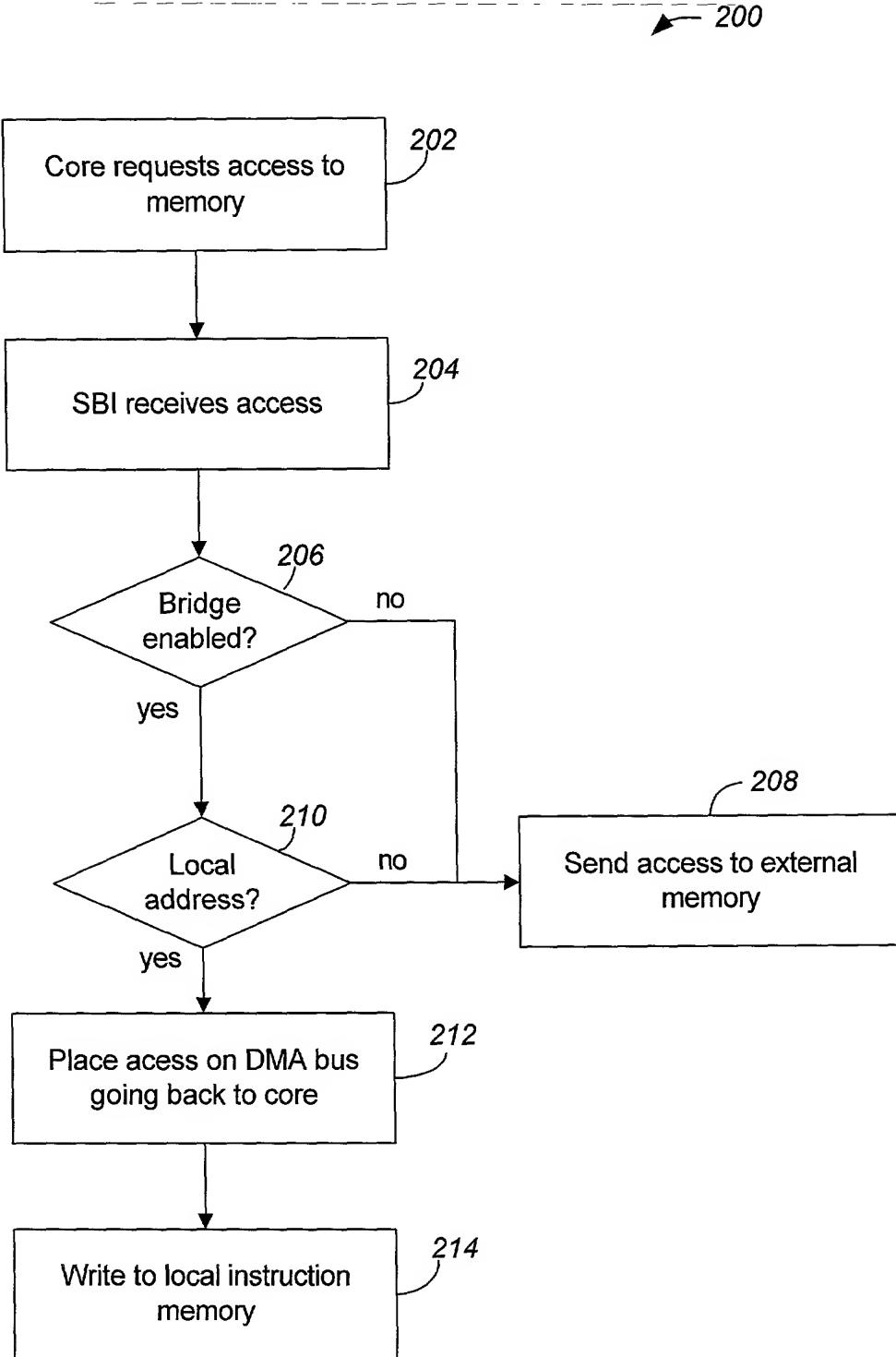
— 200

```
            ┌─────────────────────┐
            │  Core requests      │  202
            │  access to memory   │
            └─────────┬───────────┘
                      │
                      ▼
            ┌─────────────────────┐
            │  SBI receives       │  204
            │  access             │
            └─────────┬───────────┘
                      │
                      ▼
                   206
                  ╱ Bridge ╲        no
                 ╱ enabled? ╲──────────────┐
                 ╲          ╱               │
                  ╲        ╱                │
                   yes                      │
                    │                       │
                    ▼                       ▼
                  210               ┌──────────────────┐
                 ╱ Local ╲    no    │  Send access to  │  208
                ╱ address?╲────────▶│  external memory │
                ╲         ╱         └──────────────────┘
                 ╲       ╱
                  yes
                   │
                   ▼
            ┌─────────────────────┐
            │  Place acess on     │  212
            │  DMA bus going      │
            │  back to core       │
            └─────────┬───────────┘
                      │
                      ▼
            ┌─────────────────────┐
            │  Write to local     │  214
            │  instruction memory │
            └─────────────────────┘
```
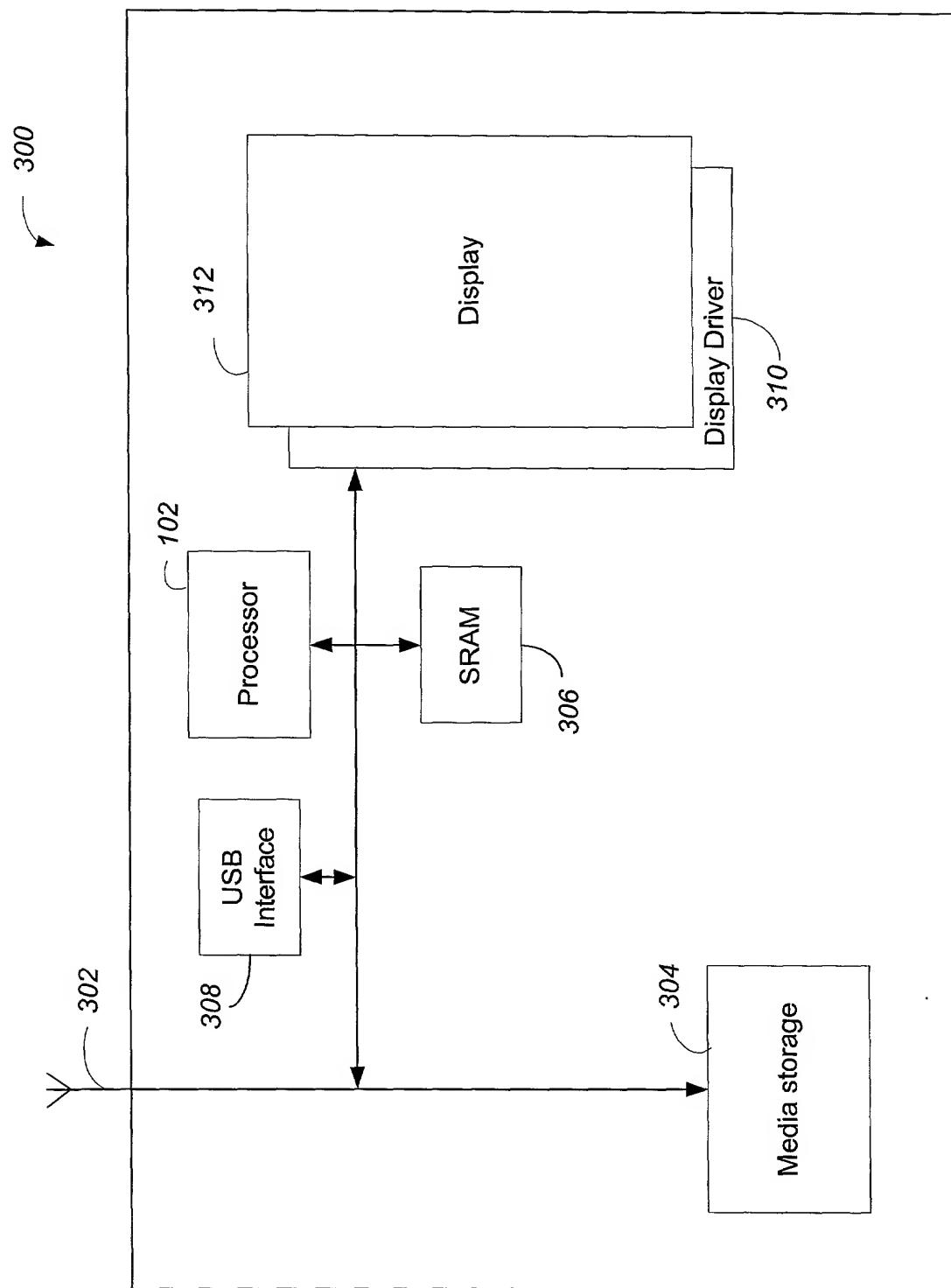
FIG. 2

FIG. 3